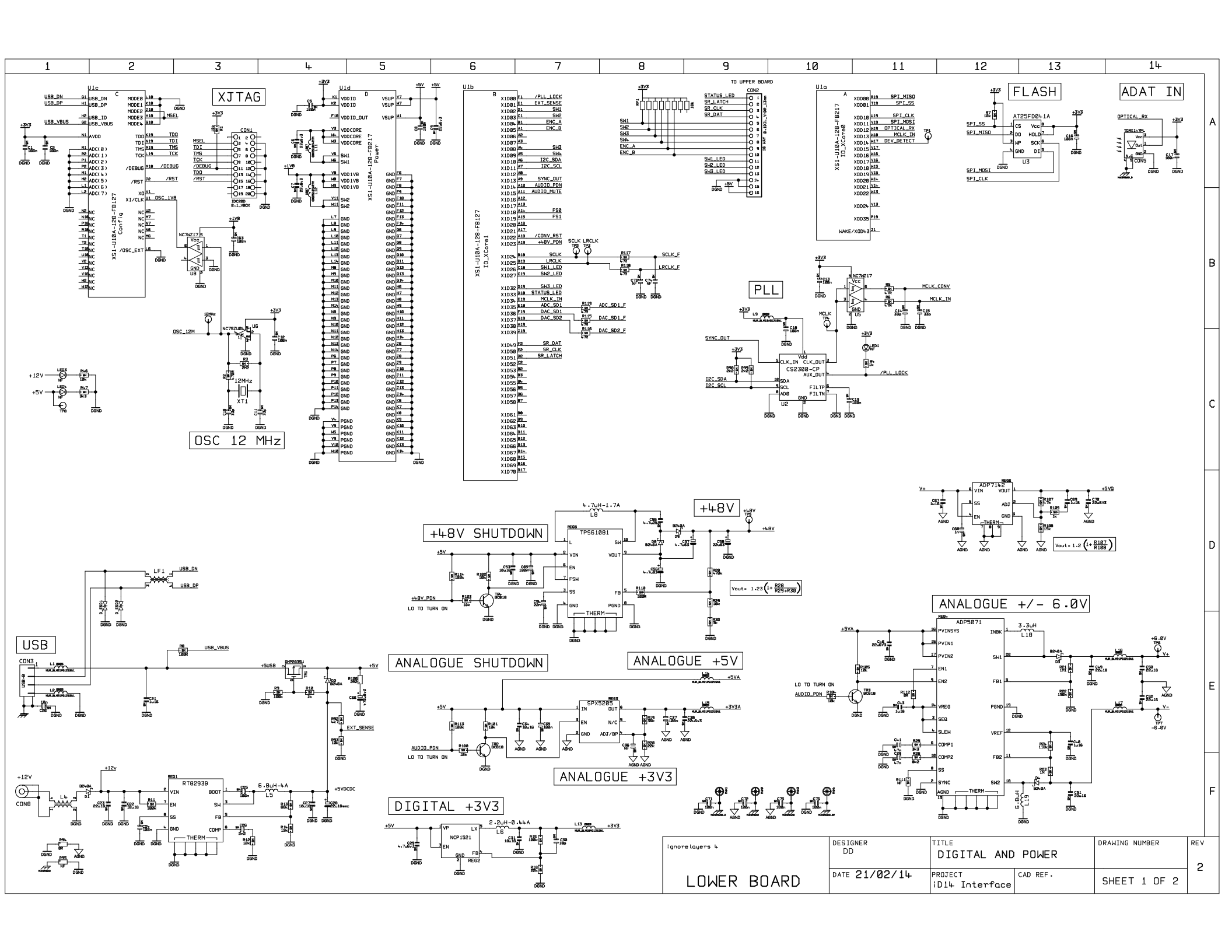
XJTAG
OSC 12 MHz
FLASH
ADAT IN
PLL
+48V SHUTDOWN
+48V
ANALOGUE SHUTDOWN
ANALOGUE +5V
ANALOGUE +3V3
ANALOGUE +/- 6.0V
DIGITAL +3V3
USB
TO UPPER BOARD
LOWER BOARD
DESIGNER DD
DATE 21/02/14
TITLE DIGITAL AND POWER
PROJECT iD14 Interface
CAD REF.
DRAWING NUMBER
SHEET 1 OF 2
REV 2